



FACULTY OF ENGINEERING AND COMPUTING

Programme Regulations 2018-2019

Programme Title:	MEng in Electronic and Computer Engineering
Programme Code	MECE
Offered on a full-time or part-time basis	Full Time (01) Part Time (02)

Note: *Programme Regulations should be read in conjunction with Marks and Standards which can be found at <http://www.dcu.ie/registry/examinations/index.shtml>*

1. Programme Specific Rules and Requirements

1.1 Calculation for the Award Classification

To be eligible to graduate with the award of MEng in Electronic and Computer Engineering, candidates must pass 90 credits on this programme, with a minimum of 60 credits at Level 9, with a maximum of 30 credits at Level 8, and including a project of 30 Level 9 credits.

In this programme regulations document, the word “major” means that a substantial proportion of a student’s course of study is taken in a specialized subject area. Students may graduate with an MEng in Electronic and Computer Engineering with a Major in Nanotechnology, or a Major in Internet of Things (IoT), or a Major in Image Processing and Analysis or with a Major in Advanced Data Networks, provided that, in addition to requirements in the above paragraph, (a) they pass a project deemed to be in an area of the Major, (b) they pass 30 credits of taught modules at Level 9 deemed to be in the area of the Major, and (c) they pass all modules deemed to be required modules for the Major. Candidates may be deemed to have met requirements (b) and/or (c) during their undergraduate studies, in which case they are exempt from them. Then, the credits involved may not be used to meet the requirements of the paragraph above.

When calculating a candidate’s precision mark, each 7.5 credit is weighted at 150 points (and pro rata for modules with other than 7.5 credits), the project is weighted at 600 points. If a candidate has passed more than eight modules, then the precision mark is

based on all first attempts up to the time where the candidate has attempted 60 taught credits. Subsequent module passes are treated as repeat attempts. The candidate's first attempt at a project is used when calculating the precision mark. Subsequent project attempts are treated as repeat attempts, irrespective of which project module code the candidate is registered on.

Project work is permitted over the summer months.

Candidates who pass 60 credits at Level 9, may exit with a Graduate Diploma in Electronic and Computer Engineering. Candidates who pass 30 credits at Level 9 may exit with a Graduate Certificate in Electronic and Computer Systems. These exit awards are made without a Major.

The students who have already passed modules from the current MEng academic structure and have used them as credit for a different qualification, cannot use them towards the MEng, Graduate Diploma or Graduate Certificate. However, the credits can be used to meet the prerequisites of any of the Majors.

2. Derogations from Marks and Standards

Marks and Standards apply.

3. Progression

This is a continuous programme. Once a candidate has met the programme requirements they will graduate.

4. Compensation

Marks and Standards apply.

5. Resit Categories

The resits offered for the August examinations diet vary depending on the module to be re-taken. The following is an explanation of the resit categories.

Resit category 1: A resit is available for all components of the module

Resit category 2: No resit is available where the module is 100% assessed by Continuous Assessment

<i>Module Code</i>	<i>Module Title</i>
EE580	Masters Project-Internet of Things Major

EE581	Masters Project-Nanotechnology Major
EE592	General Masters Project
EE595	Masters Project-Image Processing & Analysis Major
EE596	Masters Project-Network Implementation Major
EE607	Study Abroad 2

Resit category 3: No resit is available for the continuous assessment component and the examination must be re-taken.

<i>Module Code</i>	<i>Module Title</i>
EE454	Optical Communications System Design
EE463	Solid State Electronic & Semiconductor Devices
EE495	Transmission Lines, RF Propagation & Radio Link Design
EE506	Fundamentals of Photonic Devices
EE559	Fundamentals of Nanoelectronics Technology